

THE PARALLEL HTM SPATIAL POOLER WITH ACTOR MODEL

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ABSTRACT

The Hierarchical Temporal Memory Cortical Learning Algorithm (HTM CLA) is an algorithm inspired by the biological functioning of the neo-cortex, which combines spatial pattern recognition and temporal sequence learning. It organizes neurons in layers of column-like units built from many neurons such that the units are connected into structures called regions (areas). Layers can be hierarchically organized and can further be connected into more complex networks, which would allow to implement higher cognitive capabilities like invariant representations. However, a complex topology and a potentially high number of neurons would require more computing power than a single machine even with multiple cores or a GPU could provide. This paper aims to improve the HTM CLA by enabling it to run on multiple nodes in a highly distributed system of processors; to achieve this we use the Actor Programming Model. The proposed concept also makes use of existing cloud and server less technology and it enables easy setup and operation of cortical algorithms in a distributed environment. The proposed model is based on a mathematical theory and computation model, which targets massive concurrency. Using this model drives different reasoning about concurrent execution and should enable flexible distribution of cortical computation logic across multiple physical nodes.

This work is the first one about the parallel HTM Spatial Pooler on multiple nodes with named computational model. With the increasing popularity of cloud computing and serverless architectures, this work is the first step towards proposing interconnected independent HTM CLA units in an elastic cognitive network. Thereby it can provide an alternative to deep neuronal networks, with theoretically unlimited scale in a distributed cloud environment. This paper specifically targets the redesign of a single Spatial Pooler unit.

KEYWORDS

Hierarchical Temporal Memory, Cortical Learning Algorithm, HTM CLA, Actor Programming Model, AI, Parallel, Spatial Pooler.

1. INTRODUCTION

Currently more and more popular Artificial Neural Networks employ supervised learning based on strong mathematical principles. These principals are efficient to solve specific kind of problems, but they operate in a way, which is not very well aligned with the way the brain might work.

Similarly, Recurrent Neural Networks are increasingly closing in to model of the biological functioning of parts of the brain. Unlike the brain, which typically operates in an unsupervised way, concepts like RNN and DNN apply supervised learning techniques.

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Hierarchical Temporal Memory Cortical Learning Algorithm (HTM CLA) [1] is an algorithm aiming to replicate the functioning of neocortex [2]. It incorporates a spatial pooler algorithm capable of learning spatial patterns by using simple Hebbian learning rules [3]. Every time the same or a similar input recurs, synapses between the input and columns strengthen their permanence (weight) value [4]. Similarly, synapses can “forget” a learned pattern if it does not occur for a long enough time.

Such hierarchically organized structures can also be connected into networks, which provide more cognitive capabilities like invariant representation, pattern- and sequence-recognition.

The original HTM CLA was implemented in Python as a part of the NUPIC framework developed by Numenta [5]. C++ and JAVA implementations of HTM CLA are also available.[6] Because many of the modern enterprise applications are typically implemented in .NET with an increasing demand for cross-platform (Linux, Windows and MacOS) support, an implementation of this algorithm is required in the software industry to avoid inefficient and costly bridging between frameworks. As a preceding part of this work, HTM CLA was ported to C# .NET Core. [7] The current C# .NET Core version of HTM CLA aligns with JAVA implementations (which aligned with the original Python version [1]). It supports the single-core **Spatial Pooler** and **Temporal Memory** algorithms, which are limited in some ways. Processing of information in neurons inside of HTM is sparsely encoded as in biological neuronal circuits [8]. HTM CLA, in a nutshell, uses Hebbian learning rules [9] on binary sparse arrays represented as sequence of integers (0/1). In the context of memory consumption, the current representation of binary values in the form of integers is not the most efficient. Improving this is still work in progress and this kind of optimization is not in the focus of this paper. The assumption in the present work is rather that the current algorithm, when used with a high number of neurons, is highly CPU and RAM intensive. The simple Hebbian-Rule makes internal calculations efficient in comparison to other algorithms (i.e. back-propagation). However, the complex topology of the Spatial Pooler (SP) and the Temporal Pooler (TP) in HTM CLA with a high number of neurons and synaptic connections, internal inhibition, and boosting–algorithms, requires significantly more computing power and memory than available on a single commodity machine with multiple core processors and a GPU.

Current implementations across the mentioned frameworks maintains internally several matrices and maps (key-value pairs). For example, there is a matrix of synaptic connections between input neurons and cortical columns. To create a **Spatial Pooler** instance with 128x128 sensory neurons and 1024x1024 columns, the framework will have to create this matrix with $16,384 \times 1,048,576 = 17,179,869,184$ elements. In a .NET framework using a 64bit architecture operating system, the maximum possible array size of integers (32 bits) is 2,147,483,591, calculated as:

$$\frac{2^{32}}{2} - 56 = 2.147.483.591 \quad (1)$$

This is a half of the maximal integer value on 64 systems subtracted by 56, which is an internal framework overhead to hold an array. This limit depends on many factors and fortunately, can be optimized. Nonetheless even with a very efficient optimization, the limitations from using a single node only will remain.

The current architecture of HTM CLA has, in this context, two limitations of interest: a limitation of the synaptic matrix size by **available memory** and long **calculation times** required for operations on synapses. Most papers related to HTM CLA indicate experimental work with 1024, 2048, 4096 (see [10]) and 16384 columns. As an example, in a case of 4096 columns and sensory input of 1024 neurons, which corresponds to an input image of 32x32 pixels, the SP algorithm will create 4,194,304 synapses when using global inhibition. The goal is therefore to

design a distributed HTM CLA, which can run on multiple nodes and operate with any number of columns (i.e. >100,000). HTM CLA is redesigned for flexible horizontal scale in highly distributed systems by using an actor model. Moreover, the concrete implementation should make usage of modern container serverless technologies. Such a model should enable the flexible distribution of computation inside of a single HTM CLA unit or connecting multiple independent HTM CLA units in collective intelligence networks and provide an alternative to deep neuronal networks. This work is the first one about the parallel HTM Spatial Pooler on multiple nodes with the Actor Programming Model. With the increasing popularity of cloud computing and serverless architectures, this work is the first step towards proposing interconnected independent HTM CLA units in an elastic cognitive network and can provide an alternative to deep neuronal networks, with theoretically unlimited scale in a distributed cloud environment. This paper specifically targets the redesign of a single Spatial Pooler unit.

Section 2 in this paper describes the current state of the Actor Programming model and the Spatial Pooler. Sections 3 and 4 describe how the Spatial Pooler was improved for scale with help of Actor Model reasoning.

2. ACTOR PROGRAMMING MODEL AND CURRENT STATE OF THE SPATIAL POOLER

2.1 Actor Programming Model

The Actor Programming Model [11] is a mathematical theory [6] and computation model, which addresses some of the challenges posed by massive concurrency. In this theory, the Actor is treated as the universal primitive of concurrent computation. An Actor is a computational unit that, in response to a message it receives, can concurrently run code. Motivation for this programming model in this work is the simple reasoning about concurrent computation. Moreover, both the HTM CLA and the Actor Model are biologically inspired models.

Designing distributed systems with this model can simplify compute balancing between actors deployed on different cores and physically distributed nodes. In this paper, a node is defined as a physical machine, which hosts multiple actors.

Because this work is related to the C# .NET Core implementation of Spatial Pooler and Temporal Memory, the Actor Model implementation must support the .NET Core platform. Following a review of several actor programming model frameworks, including Orleans Virtual Actors [12], Service Fabric Reliable Actors [13], and Akka.NET [14], it became apparent that none of them is suitable for this task. While they are very powerful, such solutions do not offer custom partitioning functionality [12] [13], or they rely on some corporate-specific cluster [13]. As a result, the approach taken was to design and implement a lightweight version of the actor model framework. The most promising framework was Akka.NET [14], but it has shown insufficient results when it comes to networking under high CPU consumption. The Actor Model Framework proposed by this paper combines RPC and API style messaging to enable an easy and intuitive implementation. Message API style is used to physically decouple nodes in the cluster, which enables easier addition of new nodes while the system is running.

Immediately after start of the application, the actor local system is instantiated. This system drives the calculation and plays a role of scatter and gather. Then the reference to the remote instance of the Actor is created, which typically implements the calculus code. The reference behaves as a proxy to the actual implementation, which is running elsewhere in the cluster. Finally, the remote calculation is started by using a generalized Ask method. This method routes

the request to the Actor running on some node in the cluster, requests the calculation and awaits a result. The implementation of all distributed calculations is grouped together in a class called *HtmActor*; calculations are triggered by appropriate messages, i.e. *CalculateOverlap*, *AdaptSynapses*, etc. More details about calculus? can be found in section 3. The ‘cluster’, which hosts the actors, is built in a generalized manner, with no knowledge of HTM CLA, and can be used for any other kind of distributed calculation. The logic inside the cluster is used to receive messages, find actors with appropriate calculus, execute calculus and send back the result of calculation. The same code with the same configuration executes on every physical node in the cluster.

2.2 Current State of the Spatial Pooler

The original implementation of the Spatial Pooler (SP) and the Temporal Memory (TM), as originally migrated from JAVA, supports in .NET Core the single threaded execution of calculus of both algorithms. To be able to support parallel execution of HTM on multicore and multimode infrastructures, the SP algorithm was initially redesigned to support multiple cores on a single machine. Note that the optimization of the TM algorithm is work in progress and it is beyond the scope of this paper. The sensory input is defined as a set of neurons by input topology; Spatial Pooler uses an internally flattened version of input vector mapped to sensory neurons. Every input is represented as a set of values (0/1), where N is the number of all sensory neurons. This number is also known as the number of features. A flattened version of the input vector I_k is defined as:

$$I_k = \{0,1\}^{1 \times N} \quad (2)$$

Columns are defined as a set of grouped cells, represented as a flat column array, where M is the total number of columns:

$$C = \{c1, c2, \dots, cM\}^{1 \times M} \quad (3)$$

Most other neuronal networks typically connect every column to every input neuron. The **Spatial Pooler** connects to a subset of input neurons. This subset is defined by receptive field (**RF**) of the column. **RF**-array is defined as a subset of all column’s synapses:

$$P_k^{1 \times Ckq} | C_{kq} \in \{0, \dots, N-1\} \quad (4)$$

The original design of SP maintains a single instance of the connection matrix λ . This matrix specifies whether the column C_i is connected to the sensory neuron I_j . Indexes i and j are in the flattened versions of columns and sensory neurons respectively.

$$\lambda = \begin{pmatrix} x_{11} & x_{12} & \dots & x_{1N} \\ x_{21} & & & \\ \dots & & & \\ x_{M1} & x_{M2} & & x_{MN} \end{pmatrix} | x_{ij} \in \{0,1\} \quad (5)$$

Note, that the C_i column is connected to the sensory neuron I_j if the synapse permanence value is higher than the *proximal synapse activation* threshold. More details about the mathematical formalization of HTM CLA can be found in [15].

3. PROPOSED SPATIAL POOLER FOR PARALLEL EXECUTION WITH ACTOR MODEL

To be able to save memory and partition calculus of entire column space, This matrix has been removed from original version of **SP** and semantically restructured as a graph of columns, in order to be able to save memory and partition calculus of the entire column space.

Figure 1 shows a single column inside of the column graph.

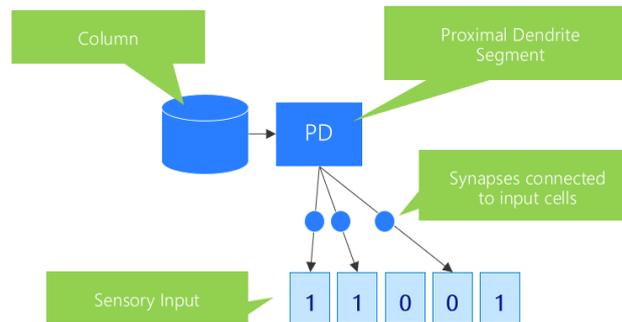


Figure 1

Representation of a single column, which can be calculated on any node in the cluster. Every column holds its own dendrite segment with a set of synapses connected to sensory input defined by its Receptive Field.

Removal of this matrix enabled easier reasoning about single column calculus, as proposed by the Actor model approach. With this, it is possible to partition columns and to share memory across multiple nodes without of need to use distributed locks, which must be used to coordinate distributed calculation. Right now, three implementations of **SP** are implemented and considered:

- *Spatial Pooler single threaded* original version without algorithm specific changes.
- *SP-MT multithreaded* version, which supports multiple cores on a single machine and
- *SP-Parallel*, which supports multi-core and multimode calculus of spatial pooler.

The Spatial Pooler algorithm consists in general of two stages inside of an application:

- Initialization
- Learning

Every named stage runs several specific calculations shown at *Figure 2*. For example, the Initialization stage performs a few initialization tasks internally. The Columns and synapse initialization stage creates a graph of columns with all required artefacts. The initialization stage is typically running once, and the learning stage is running for every input sample (online-learning). *SP-MT* and *SP-Parallel* versions of **SP** hold the parallel implementation of all listed algorithms as shown in *Figure 2* at the right.

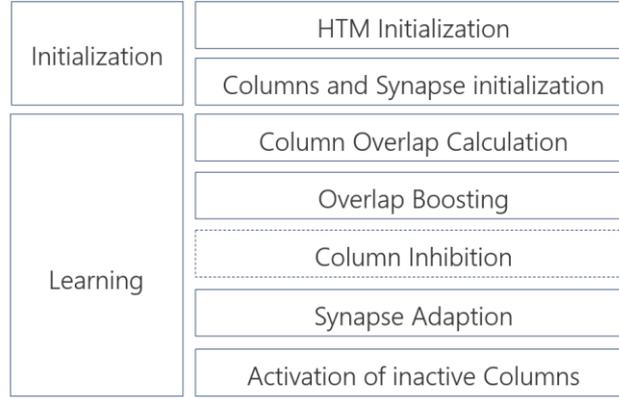


Figure 2 : Spatial Pooler internal calculations shared across Initialization and Learning stage (left). Every stage consists of multiple calculations steps (right).

Apart from *Column Inhibition*, which is currently shared across all three implementations, all other calculations are SP-version specific. In some future versions of SP, *Column Inhibition* and some other smaller algorithmic parts (not listed here) might also be redesigned. Redesign of **SP** targets two major tasks: partitioning of memory and partitioning of CPU usage. Memory consumption inside of SP is defined as follows:

$$m = m(i_k) + \sum_u^M m_c(u) \quad (6)$$

$$m_c(u) = m_{s0} + \sum_w^S m_s(w) \quad (7)$$

where:

m – Overall memory consumption of a single SP instance, while a calculation is running.

ik – Input vector sample: $k \in \{0, N - 1\}$

$m(ik)$ - Memory occupied by an input sample.

$mc(u)$ - Memory occupied by a single column.

$ms0$ - Memory occupied by column, excluding synapses. This memory is nearly the same across all columns. The difference is mainly influenced by holding references to a different number of synapses.

$ms(w)$ - Memory inside of a column occupied by single instance of a synapse. Sum fraction of equation

(7) corresponds to memory occupied by the proximal dendrite segment of a column with S synapses.

The original SP implementation and SP-MT both consume all memory m inside of a single process and it is therefore limited by physical memory of the hosting machine (node). For this reason, the first HTM Initialization step (see

Figure 2) shares the same technique to allocate required memory in both named algorithms. SP-MT algorithm runs all calculations of every column on a single core by using C# technique called task/await.

4. THE PARTITION CONCEPT

The SP-Parallel algorithm performs partitioning of the column graph and distributes columns across multiple nodes. A partition is defined as a unit of computation with occupied memory.

$$P = p(Cpu, Mem) \quad (8)$$

Creating of partitions can be expressed by the following pseudo code:

```
createPartitions(numElements, numOfPartitions, nodes)
```

```

    destNodeIndx = 0

    numPerPart =
    round((1+numElements /
    numOfPartitions));

    FOR partIndx = 0 to numOfPartitions
    OR min>=numElements

min = numPerPart * partIndx;

    maxPartEl = numPerPart *
    (partIndx + 1) - 1;

IF maxPartEl < numElements
    max = maxPartEl
ELSE
    max = numElements - 1;

    destNodeIndx =
    destNodeIndx % nodes.Count;

    destinationNode =
    nodes[(destNodeIndx++ %
    nodes.Count)];
    placement =
    (destinationNode,
    partIndx, min, max)

    map.Add(placement)
ENDFOR

return map;
```

This code ensures that all columns (*numOfElements*) are uniformly shared across specified *numOfPartitions* and second, that all partitions are shared uniformly across all specified nodes. For example, if *numElements* = 90000 (columns), *nodes* = 3 and number of partitions = 35 then 34 partitions will contain 2572 elements and the last partition will be filled up with the remaining 2552 elements.

To understand how SP is changed to fulfil parallelization requirements, the following SP pseudo code must be refactored:

```

compute(input, learn)
  overlap = calculateOverlap(input)
  if(learn)
    boostedOverlap = boostFactors*overlap
  else
    boostedOverlap = overlap

  activeCols = inhibitColumns(boostedOverlaps)

  adaptSynapses(input, activeCols)

  activateInactiveCols()

```

To solve this, several parallelization approaches [16] have been analysed. As a result, a dedicated and simple HTM column placement (distribution) algorithm has been designed based on the described partitioning mechanism.

Ideally, like neural parallelism [16] in the context of node parallelization, calculus for every column could be executed on a single core. For various practical reasons, placing of single column calculation on a core is understood as a special case. The partitioning algorithm rather places a set of columns in a partition, which is calculated on a single core across all nodes. In a generalized and simplified form, the overall calculation time can be defined as follows:

$$t = CN t_s + \frac{1}{N_c} \sum_u^{CN} t_u + CN t_g \mid m < m_\theta \quad (9)$$

Equation (9) states that the theoretical amount of time required to calculate any step (see Figure 2) is defined as the sum of scatter time t_s needed to remotely dispatch calculation, the sum of all column-specific calculations t_u divided by the number of cores N_c and gather time t_g needed to collect results. Note that the calculation time for every column t_u is statistically different, depending on the number of connected synapses on the dendritic segment.

This equation holds as long overall memory consumption on the node does not exceed the maximally allowed threshold $m\theta$. If this limit is exceeded, the operation system will generate hard-page faults, which would cause memory reallocation to disk. Because this operational state would dramatically slow down performance, algorithms should take care of proper configuration to avoid this state.

Calculation time in such a distributed system is more complex as shown in the previous equation (9).

$$t = t_{rcv} + t_{sched} + t_{start} + t_{calc} + t_{persist} + t_{end} + t_{send} \mid m < m_\theta \quad (10)$$

trcv: Time for receiving of the message, which triggers calculation

tsched: Time required by system to schedule calculation. This is usually not a trivial task to coordinate lifecycle of partitioned calculations of columns in distributed system. All messages must be ordered and when possible, distributed locks shall be avoided. To solve this problem, already named a dedicated Actor Programming Framework was implemented on top of Microsoft Azure Service Bus [17], which provides messaging platform with many features required for this task. In this case message-session is used to ensure that all messages are ordered and dispatched to a single location, where calculation is performed. With this, no distributed locks are possible, and every partition calculation is running on a single thread. Because of this, *tsched* is taken out of algorithm and it remains a part of messaging system.

In this concept, one partition is defined as an Actor, sometimes called *partition Actor*. It physically owns 1-N columns (as shown in

Figure 1) and it performs calculus over space P_k as defined by equation (4) owned columns only (see Figure 3). This space is much smaller than space λ defined by equation (5).

$$\{p_{k1}, p_{k2}, \dots, p_{C_{kq}} \mid C_{kq} \leq IN \quad (11)$$

The Actor Model guarantees that there is only one calculation running on a single column partition across all cores in the cluster. Every partition Actor also holds the potential pool array as defined by the equation (4) and is capable of calculating the algorithm listed in Figure 2.

The distributed HTM algorithm **SP-Parallel** performs partitioning of Actors inside of the orchestrator node, which plays the role of a scatter operation. Running of calculations in actors on remote nodes is started and awaited on multiple threads inside of the orchestrator. Finally, the Actor model executes actors on nodes in the cluster and results are collected by the orchestrator node, which now plays the role of a gather operation.

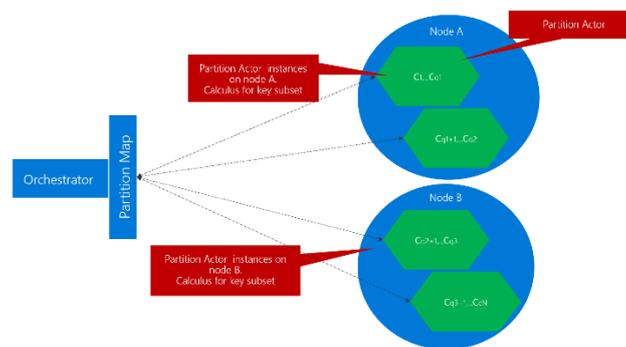


Figure 3 : Partitioned column space. Every partition is implemented as an Actor, which owns subsets of columns from the entire column graph. By providing the number of columns and nodes, the number of partitions can be automatically calculated or explicitly set.

To recap, in this partitioning concept the number of partitions and the number of nodes are known. That means, the SP-Parallel orchestrator code, which initiates placement of partitions must know nodes and can explicitly place a partition to the dedicated node. With this, the Actor model framework can ensure that full calculation is executed as a sticky session on an initiated node. This improves performance and does not require a durable persistence of the calculation state, because the state is kept in the cache.

There is also another approach, which was tested, but it was excluded from this paper. In this (second) approach the orchestrator node does not have any knowledge about the number of nodes. This enables a simpler architecture of the system, but it requires to durably store the calculation state because, after every calculation step, the next step can be initiated on another node. For this reason, nodes must be able to save and load the state to and from durable storage, which adds significant performance costs. The second approach would perform better for shorter calculations with less memory

5. EVALUATION

In this work several experiments have been created, which evaluate the performance of the modified Spatial Pooler algorithm. For all tests MNIST images of 28x28 pixels have been used. First, a single-threaded algorithm was compared against SP-MT (multicore single node SP) on different topologies (results shown for 32x32 columns).

Then the compute time of SP-Parallel was tested for a column topology 200x200 on one, two and three physical nodes. Finally, the performance of several column topologies was tested in a cluster of three physical nodes.

All tests have been executed on nodes with following “*commodity*” configuration on virtual machines in Microsoft Azure cloud: OS: Microsoft Windows Server 2016;

Processor: Intel(R) Xeon(R) CPU E5-2673 v4 @ 2.30GHz, 2295 MHz, 2 Core(s), 4 Logical Processor(s); Physical Memory (RAM): 16.0 GB.

The first experiment was designed to demonstrate performance improvements of the SP-MT versus single-threaded algorithm. Remember, both algorithms were running on a single node. As input, MNIST test images with 28x28 pixels were used, and a cortical topology of 32x32 columns. Figure 4 shows the resulting sparse representation of the MNIST image.

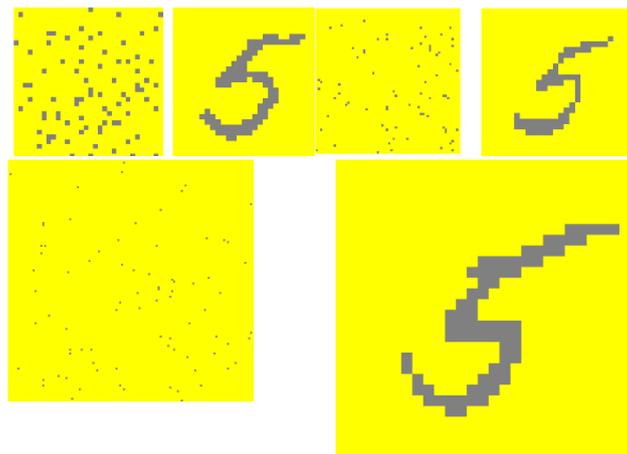


Figure 4: Sparse representations of an MNIST digit with different sparsity in column topologies 32x32 (top-left), 64x64 (top-right) and 128x128 (bottom). As an example, SDR on the top-right with column topology of 64x64 (4096 columns) occupies 2% (81) columns only.

Results shown in Figure 5 indicate that SP-MT is approximately twice faster than SP single-threaded on the indicated VM configuration.

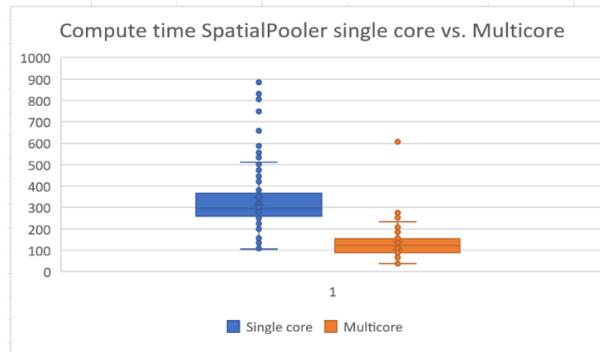


Figure 5 : Performance results, SpatialPooler single-core (SP) versus Spatial Pooler multicore (SP-MT) on a single machine. Tested on Surface Book2 with Microsoft Windows 10 Enterprise, Processor Intel(R) Core(TM) i7-8650U CPU @ 1.90GHz, 2112 MHz, 4 Core(s), 8 Logical Processor(s). MNIST 28x28 test image used 32x32 columns.

In the same experiment, the memory consumption (see Figure 6) and processing time in milliseconds in dependence of column topology were measured (see Figure 7).

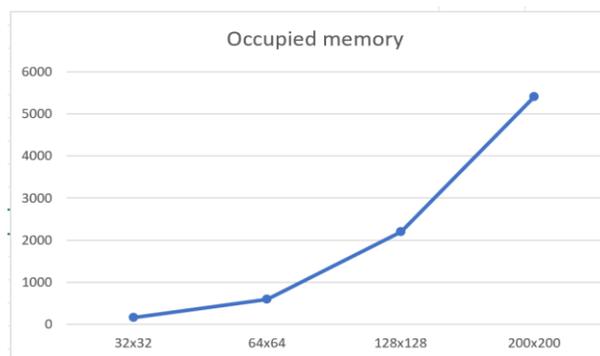


Figure 6 : RAM memory in MB, occupied during execution of the SpatialPooler-MT algorithm on a single node.

By using the same experiment with SP-Parallel instead of SP-MT, topologies with a higher number of columns and multiple nodes were tested. In this experiment learning of the MNIST image was measured on 1, 2 and 3 nodes. As shown in Figure 8 SP-Parallel on a single node needs nearly the same time as SP-MT. This is a good result because it approves that the Actor model framework does not spend significant time on the internal messaging mechanism.

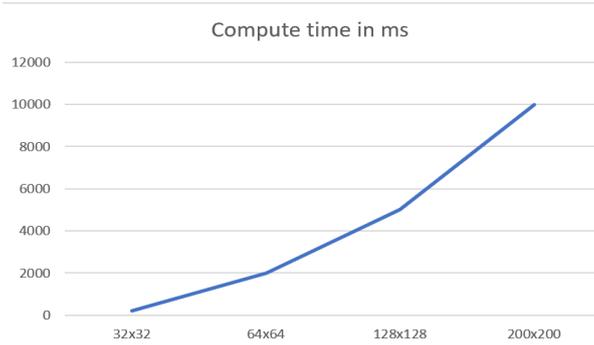


Figure 7 : Spatial Pooler-MT compute time in milliseconds in dependence of column topology.

By adding more nodes to the cluster, performance increases as expected. The optimal number of partitions still must be investigated. As for now, to ensure that calculations on multiple partitions can run in parallel, it should be 2 or 3 times higher than the number of cores on all nodes.

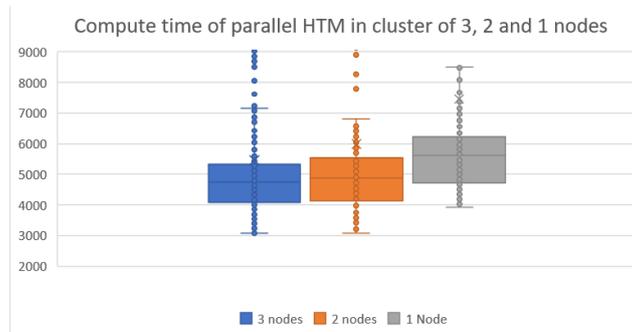


Figure 8 : Learning time in [ms] of a MNIST image 28x28 with 200x200 Columns. Performance on a single node corresponds nearly to performance of SP-MT algorithm.

Figure 9 shows memory and CPU consumption on a single node, while calculation is running on multiple nodes. Independent of column topology, both memory and CPU consumption are shared across nodes in the cluster. As shown by the figure, during initialization time (step 1) memory is increasing, while allocating space for the columns and then it gets stable across the remaining repeating steps 2, 3 and 4 during the iterative learning process.

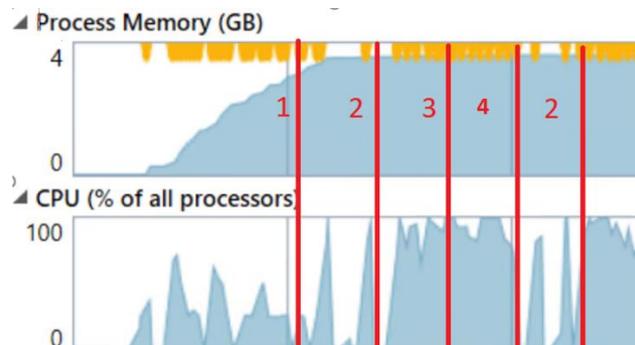


Figure 9 : Process memory on a node while the computation of SP-Parallel is running. At the beginning 1 initialization stage is running, which allocates the required memory. Subsequently, stages 2,3 and 4 are related to overlap, synapse adaption and activation of inactive columns.

Finally, the system was tested to run up to 250000 cortical columns. This configuration allocates 196000000 synapses to sensory input (28x28 sensory neurons) on a proximal dendrite, by used global inhibition of the Spatial Pooler in this experiment.

In this experiment, every column connects to all sensory neurons, which corresponds to a potential connectivity radius of 100%. Topologies 200x200, 300x300 and 500x500 (250000 columns) columns were compared.

Additionally, the initialization time (see Figure 11) of the Spatial Pooler should not be underestimated. Allocating cortical columns and corresponding synapses takes significantly more time than compute time. Note that compute times for topology 200x200 with 20 and 15 partitions do not indicate significant differences. This is because the number of partitions is higher than the number of cores (3 nodes with 4 logical processors) in both cases.

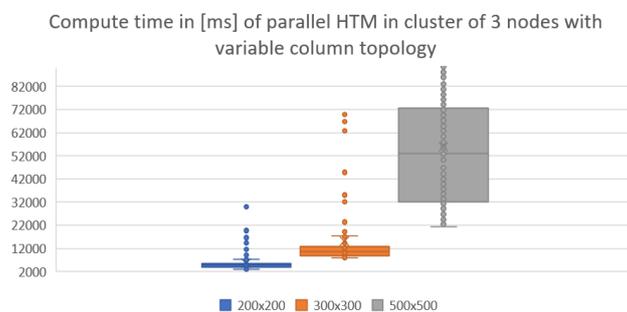


Figure 10 : Compute time of SP-Parallel on three nodes in dependence of column topology.

Having a lower number of partitions than the number of cores would not use available CPU power and having a too high number of partitions would generate too many context switches and slows down the performance.

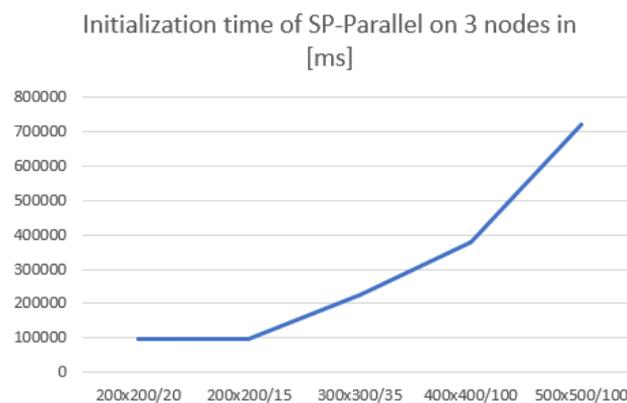


Figure 11 : Initialization time in milliseconds of SP-Parallel in a cluster of 3 nodes in dependence of column topology. Used topologies are 200x200 with 20 partitions, 200x200 with 15 partitions etc.

All presented results were tested with the Actor model implementation, which sticks calculation to specific nodes without state persistence. Persistence of calculations would slow down calculation time. Some additional experiments show (not presented in this paper) that a single column takes approx. 700kb space persisted as JSON. Persisting of partitions described in this

paper with thousands of columns would take gigabytes of space and would require a sophisticated algorithm to save and load such state in a very short time. This is one of the future tasks in this context.

6. CONCLUSIONS

Results in this paper show that HTML CLA can efficiently be scaled with an Actor programming model by using higher-level programming languages. Proposed algorithms SP-MT and SP-Parallel can successfully run on multicore and multi-node architectures on commodity hardware, respectively. SP-MT executes on a single node multicore architecture without the Actor Programming Model, which is rather used by SP-Parallel. The modified version of the Spatial Pooler can observe calculations for a high number of cortical columns in the simple Actor model cluster on commodity hardware. The building of algorithms natively in hardware by using lower-level programming languages might show better performance. However, using widely industrial accepted and extremely productive higher-level programming languages enable easier use of compute power of modern cloud environments and enables this technology for use in a wide community of developers. The current version of SP-Parallel and SP-MT rely on the same code base, which will be step by step optimized, for example, in the way how internal sparse representations are implemented, especially when it comes to memory consumption. The goal of this work was to redesign Spatial Pooler for the Actor Programming Model by enabling it for easy horizontal scaling of the multiple nodes. The current implementation supports Windows, Linux and macOS on almost any kind of hardware.

With this approach, cortical regions can be widely distributed across many machines with acceptable costs and performance. Scaling of the Spatial Pooler algorithm is the first step in this research. **Spatial Pooler** produces sparse representations of inputs in the form of active columns. By following findings in neurosciences, generated *sparse representation* can be used as an input for the **Temporal Memory** algorithm. A next step in this research is the design of a highly scalable parallel version of the Temporal Memory algorithm and the design of a cortical network with the used Actor Programming Model approach. Such cortical networks will be capable to build highly interconnected cortical regions distributed in cluster. The high degree of connections should enable powerful sequence learning and more cognitive capabilities.

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